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L49	l16 and (((simultaneous\$2 or concurrent\$2) with ((read\$3 or load\$3) and (stor\$3 or writ\$3)))) same ((ram or sdram or dram or sram) and (non-volatile or nonvolatile or flash or eeprom)))	14	L4
L48	l16 and (((simultaneous\$2 or concurrent\$2) with ((read\$3 or load\$3) and (stor\$3 or writ\$3)))) same ((volatile or ram or sdram or dram or sram) and (non-volatile or nonvolatile or flash or eeprom)))	32	L4
L47	l16 and ((smartcard or smart-card or (single adj1 package) or (smart adj card)) same ((volatile or ram or sdram or dram or sram) and (non-volatile or nonvolatile or flash or eeprom)))	24	L4
L46	l16 and (smartcard or smart-card or (single adj1 package) or (smart adj card))	86	L4
L45	l16 and (smartcard or smart-card or (single adj1 package))	40	L4
L44	L42 and (ram)	4	L4
L43	L42 and (ram with table)	3	L4
L42	6684289.pn. or 20040123020.pn. or 20040111555.pn. or 20040064636.pn.	4	L4
L41	l39 and ((volatile or ram or sdram or dram or sram) and (non-volatile or nonvolatile or flash or eeprom))	18	L4
L40	l39 and pseudo\$	3	L4

<u>L39</u>	l16 and (page with (spare and data))	24	<u>L3</u>
<u>L38</u>	l36 and L37	41	<u>L3</u>
<u>L37</u>	L16 and ((non-volatile or nonvolatile or flash or eeprom) same((partition\$3 or area or location) with (data and (backup or redundant or parity or ecc))))	95	<u>L3</u>
<u>L36</u>	L16 and ((volatile or ram or sdram or dram or sram) same((partition\$3 or area or location) with (data and (backup or redundant or parity or ecc))))	69	<u>L3</u>
<u>L35</u>	L34 and l33	87	<u>L3</u>
<u>L34</u>	L16 and ((volatile or ram or sdram or dram or sram) same((partition\$3 or area or location) same (data and (backup or redundant or parity or ecc))))	143	<u>L3</u>
<u>L33</u>	L16 and ((non-volatile or nonvolatile or flash or eeprom) same((partition\$3 or area or location) same (data and (backup or redundant or parity or ecc))))	166	<u>L3</u>
<u>L32</u>	L16 and ((non-volatile or nonvolatile or flash or eeprom) and ((partition\$3 or area or location) same (data and (backup or redundant or parity or ecc))))	379	<u>L3</u>
<u>L31</u>	L16 and ((non-volatile or nonvolatile or flash or eeprom) with (data and (backup or redundant or parity or ecc))))	255	<u>L3</u>
<u>L30</u>	L29 and l28	6	<u>L3</u>
<u>L29</u>	L16 and ((non-volatile or nonvolatile or flash or eeprom) with ((partition\$3 or area or location) with(data and redundant)))	25	<u>L2</u>
<u>L28</u>	L16 and ((volatile or ram) with ((partition\$3 or area or location) with(data and redundant)))	8	<u>L2</u>
<u>L27</u>	L16 and ((volatile or ram) with ((partition\$3 or area or location) near3(data and redundant)))	2	<u>L2</u>
<u>L26</u>	L16 and ((volatile or ram or sdram or dram or sram) with ((partition\$3 or area or location) with ((data or bit\$1) near3 (ecc or parity or control or management)))) and ((non-volatile or nonvolatile or flash or eeprom) with ((partition\$3 or area or location) with ((data or bit\$1) near3 (ecc or parity or control or management))))	36	<u>L2</u>
<u>L25</u>	L16 and ((volatile or ram or sdram or dram or sram) with ((partition\$3 or area or location) with (ecc or parity or control or management))) and ((non-volatile or nonvolatile or flash or eeprom) with ((partition\$3 or area or location) with (ecc or parity or control or management)))	131	<u>L2</u>
<u>L24</u>	L23 and l22	5	<u>L2</u>
<u>L23</u>	L16 and ((non-volatile or nonvolatile or flash or eeprom) with ((partition\$3 or area or location) with (ecc or parity)))	22	<u>L2</u>
<u>L22</u>	L16 and ((volatile or ram or sdram or dram or sram) with ((partition\$3 or area or location) with (ecc or parity)))	15	<u>L2</u>
<u>L21</u>	L16 and ((volatile or ram) with ((partition\$3 or area or location) with (ecc or parity)))	5	<u>L2</u>
<u>L20</u>	L16 and ((volatile or ram) with ((partition\$3 or area or location) near3(ecc or parity)))	1	<u>L2</u>
<u>L19</u>	L16 and ((volatile or ram) with (ecc or parity))	83	<u>L1</u>
<u>L18</u>	L16 and ((volatile or ram) with ((ecc or parity) near2 bit\$1))	11	<u>L1</u>
<u>L17</u>	L16 and ((volatile or ram) with spare with actual)	2	<u>L1</u>
<u>L16</u>	l14 or L15	6184	<u>L1</u>
<u>L15</u>	711/170,173.ccls.	1785	<u>L1</u>
<u>L14</u>	l7 or 711/168.ccls.	4834	<u>L1</u>
<u>L13</u>	L9 and ((ram or volatile) with actual with spare)	1	<u>L1</u>
<u>L12</u>	L9 and ((ram or volatile) same actual same spare)	1	<u>L1</u>
<u>L11</u>	L9 and (actual same spare)	1	<u>L1</u>
<u>L10</u>	L9 and before	1	<u>L1</u>
<u>L9</u>	20020120820.pn.	1	<u>L9</u>
<u>L8</u>	20020120820.pn.L7	4553	<u>L8</u>
<u>L7</u>	l1 or 711/167,169.ccls.	4553	<u>L7</u>

<u>L6</u>	l4 and ((volatile or ram or sdram or dram or sram) and (non-volatile or nonvolatile or flash or eeprom))	2106	<u>L6</u>
<u>L5</u>	L4 and (pseudo-access\$3 or (pseudo near2 access\$3))	6	<u>L5</u>
<u>L4</u>	l1 or l2 or L3	4453	<u>L4</u>
<u>L3</u>	714/52,702.ccls.	298	<u>L3</u>
<u>L2</u>	365/185.11.ccls.	918	<u>L2</u>
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1 A novel NVRAM cell technology for high density applications

Yasumauchi, Y.; Tanaka, K.; Sakiyama, K.;

Electron Devices Meeting, 1988. Technical Digest., International , 11-14 Dec. 1988

Pages:416 - 419

[\[Abstract\]](#) [\[PDF Full-Text \(320 KB\)\]](#) **IEEE CNF**

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Pages:812 - 815 vol.2

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6 **A manpack portable LPC 10 vocoder**

Fette, B.; Rimpo, C.; Kish, J.;

Acoustics, Speech, and Signal Processing, IEEE International Conference on ICASSP '84. , Volume: 9 , Mar 1984
Pages:786 - 789

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7 **Mode partition noise in vertical cavity surface emitting lasers**

Lee, H.L.T.; Ram, R.J.;

Advanced Semiconductor Lasers and Applications/Ultraviolet and Blue Lasers and Their Applications/Ultralong Haul DWDM Transmission and Networking/WDM Components, 2001 Digest of the LEOS Summer Topical Meetings , 30 July-1 Aug. 2001
Pages:2 pp.

[\[Abstract\]](#) [\[PDF Full-Text \(160 KB\)\]](#) [IEEE CNF](#)

8 **Soluble dodecylsulfanylphthalocyanines as sensitive coatings for chemical sensors in gas phase**

Altindal, A.; Patel, R.; Zhou, R.; Josse, F.; Ozturk, Z.Z.; Bekaroglu, O.;

Frequency Control Symposium, 1998. Proceedings of the 1998 IEEE International , 27-29 May 1998
Pages:676 - 684

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9 **A highly available replication control protocol using volatile witnesses**

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10 **Hardware realisation of artificial neural network with application to information coding**

Young, D.; Lee, A.W.H.; Cheng, L.M.;

Neural Networks, 1994. IEEE World Congress on Computational Intelligence., 1994 IEEE International Conference on , Volume: 6 , 27 June-2 July 1994
Pages:3982 - 3985 vol.6

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11 **Using volatile witnesses to extend the applicability of available copy protocols**

Paris, J.F.;

Management of Replicated Data, 1992., Second Workshop on the , 12-13 Nov. 1992
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12 The GPA machine: a generally partitionable MSIMD architecture
Bridges, T.;
 Frontiers of Massively Parallel Computation, 1990. Proceedings., 3rd Symposium
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VLSI, 2003. Proceedings. IEEE Computer Society Annual Symposium on , 20-21 Feb. 2003

Pages:107 - 112

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1 Chalcogenide memory arrays: characterization and radiation effects

Maimon, J.D.; Hunt, K.K.; Burcin, L.; Rodgers, J.;

Nuclear Science, IEEE Transactions on , Volume: 50 , Issue: 6 , Dec. 2003

Pages:1878 - 1884

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2 A survey of progress in coding theory in the Soviet Union

Kautz, W.; Levitt, K.;

Information Theory, IEEE Transactions on , Volume: 15 , Issue: 1 , Jan 1969

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3 An overview of logic architectures inside flash memory devices

Silvagni, A.; Fusillo, G.; Ravasio, R.; Picca, M.; Zanardi, S.;

Proceedings of the IEEE , Volume: 91 , Issue: 4 , April 2003

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4 SRAM based re-programmable FPGA for space applications

Wang, J.J.; Katz, R.B.; Sun, J.S.; Cronquist, B.E.; McCollum, J.L.; Speers, T.M.; Plants, W.C.;

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5 Design of a digital gauss meter for precision magnetic field measurements

Sedgwick, J.; Michalson, W.R.; Ludwig, R.;

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6 Studies of the angular correlation function of scattering by random rough surfaces with and without a buried object

Zhang, G.; Tsang, L.; Kuga, Y.;

Geoscience and Remote Sensing, IEEE Transactions on , Volume: 35 , Issue: 2 , March 1997

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7 Nonlinear clutter cancellation and detection using a memory-based predictor

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9 Some construction methods for error-correcting (d,k) codes

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10 Single event upsets for Space Shuttle flights of new general purpose computer memory devices

O'Neill, P.M.; Badhwar, G.D.;

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Pages:1755 - 1764

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11 Asymptotically optimum finite-memory detectors in ϕ -mixing dependent processes

Cheung, J.; Kurz, L.;

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Pages:2344 - 2354

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12 An iteration partition approach for cache or local memory thrashing on parallel processing

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Computers, IEEE Transactions on , Volume: 42 , Issue: 5 , May 1993

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13 Knowledge-based system for boundary detection of four-dimensional cardiac magnetic resonance image sequences

Suh, D.Y.; Eisner, R.L.; Mersereau, R.M.; Pettigrew, R.I.;

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Wang Xiande; Zhang Zhongzhi; Fu Junmei;

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